

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Frederic Boutaud GROUP: 2181  
SERIAL NO: 10/715,629 EXAMINER: Tonia Lynn Meonske Dollinger  
FILED: 11/17/2003 Date of mailing  
of Notice of  
FOR: DIGITAL SIGNAL PROCESSOR Allowance: 2/19/08  
ARCHITECTURE WITH OPTIMIZED  
MEMORY ACCESS FOR CODE DISCONTINUITY

Mail Stop Issue Fee  
Assistant Commissioner of Patents  
P.O. Box 1450, Alexandria, VA 22313-1450

Attn: Official Draftsman

**TRANSMITTAL OF FORMAL DRAWINGS**

Applicant submits herewith new drawing(s) for this application. Attached please find EIGHTEEN (18) sheets of formal drawings for this application.

The three month period of response set in the Notice of Allowability (PTOL 37) expires on May 19, 2008, and this submission is on or before this expiry date.

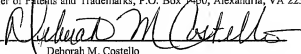
Respectfully submitted,



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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited electronically with the United States Postal Service on the date shown below addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.

  
Deborah M. Costello

Date: 5-14-08